

Fig. 1

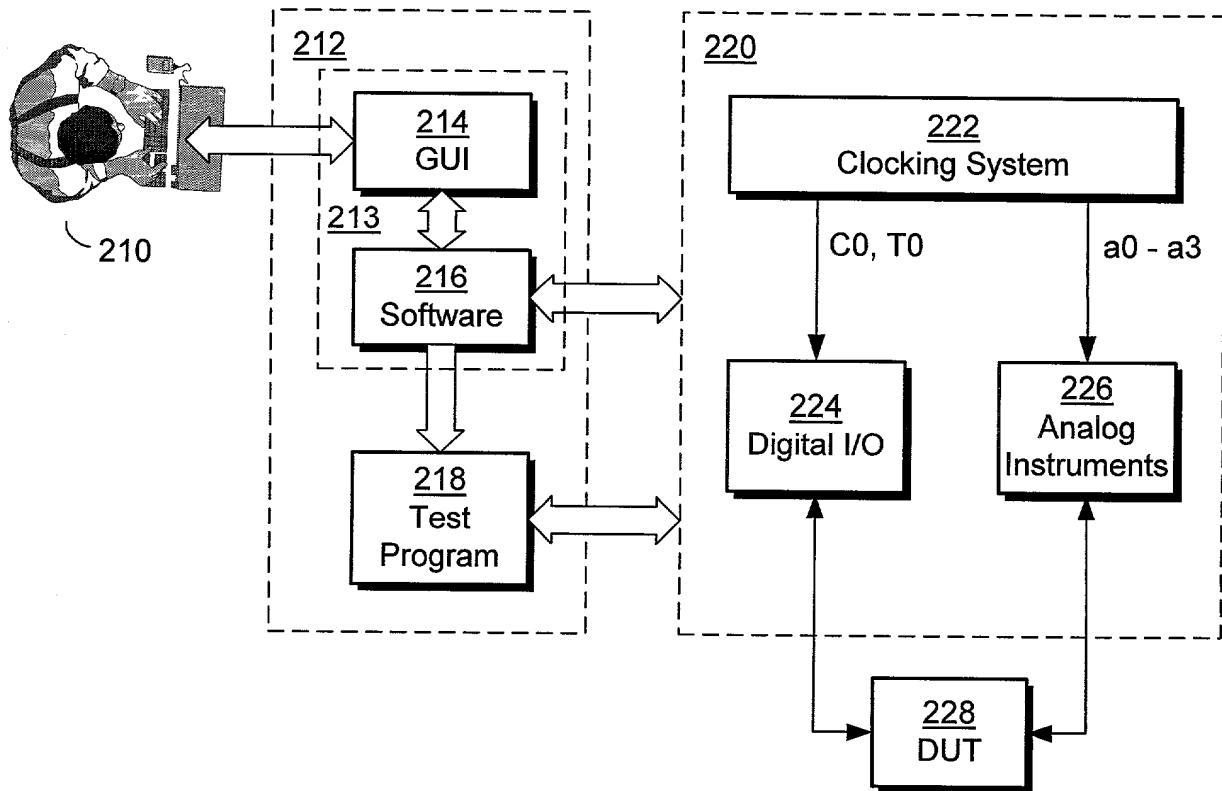


Fig. 2

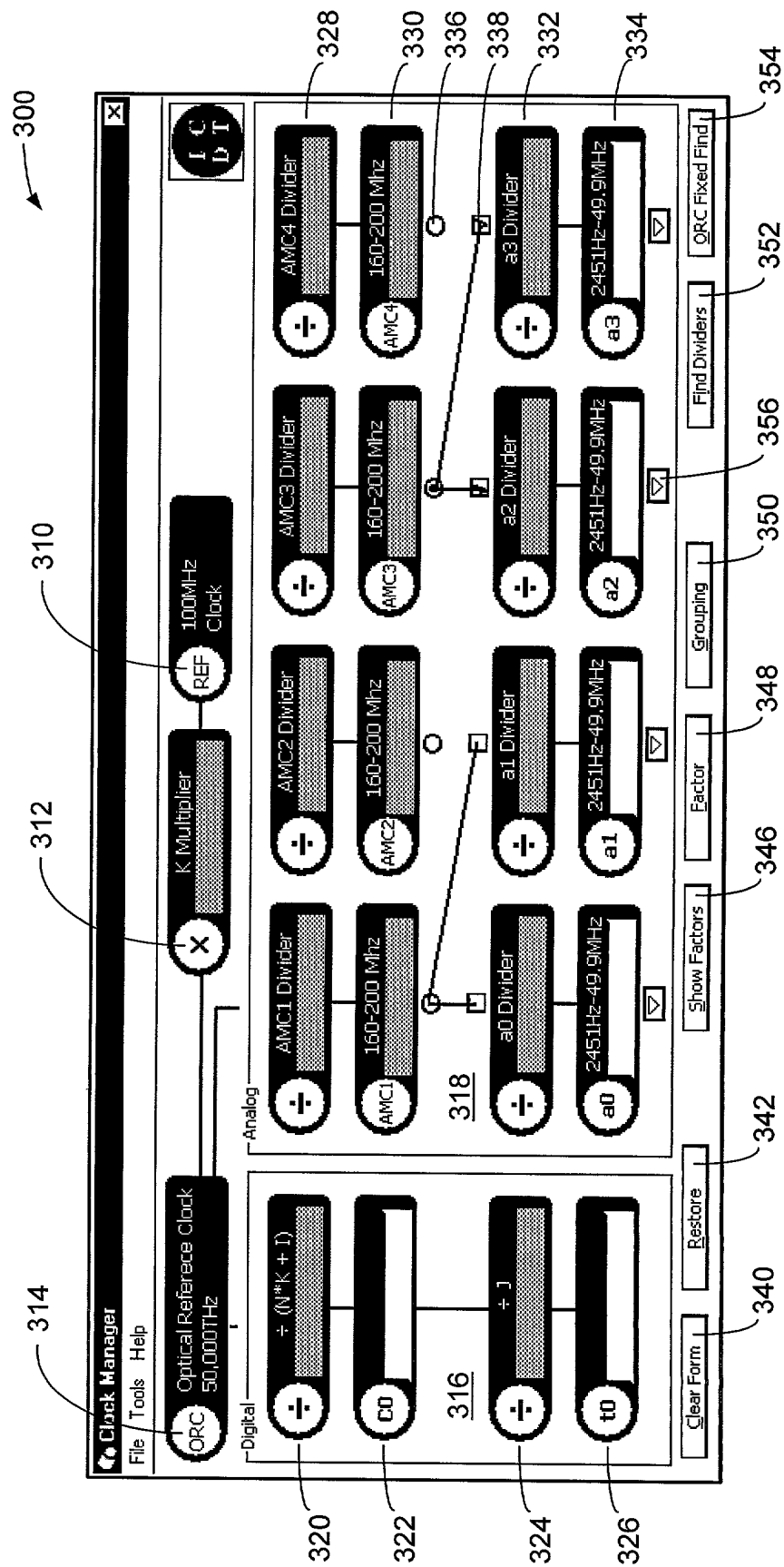


Fig. 3

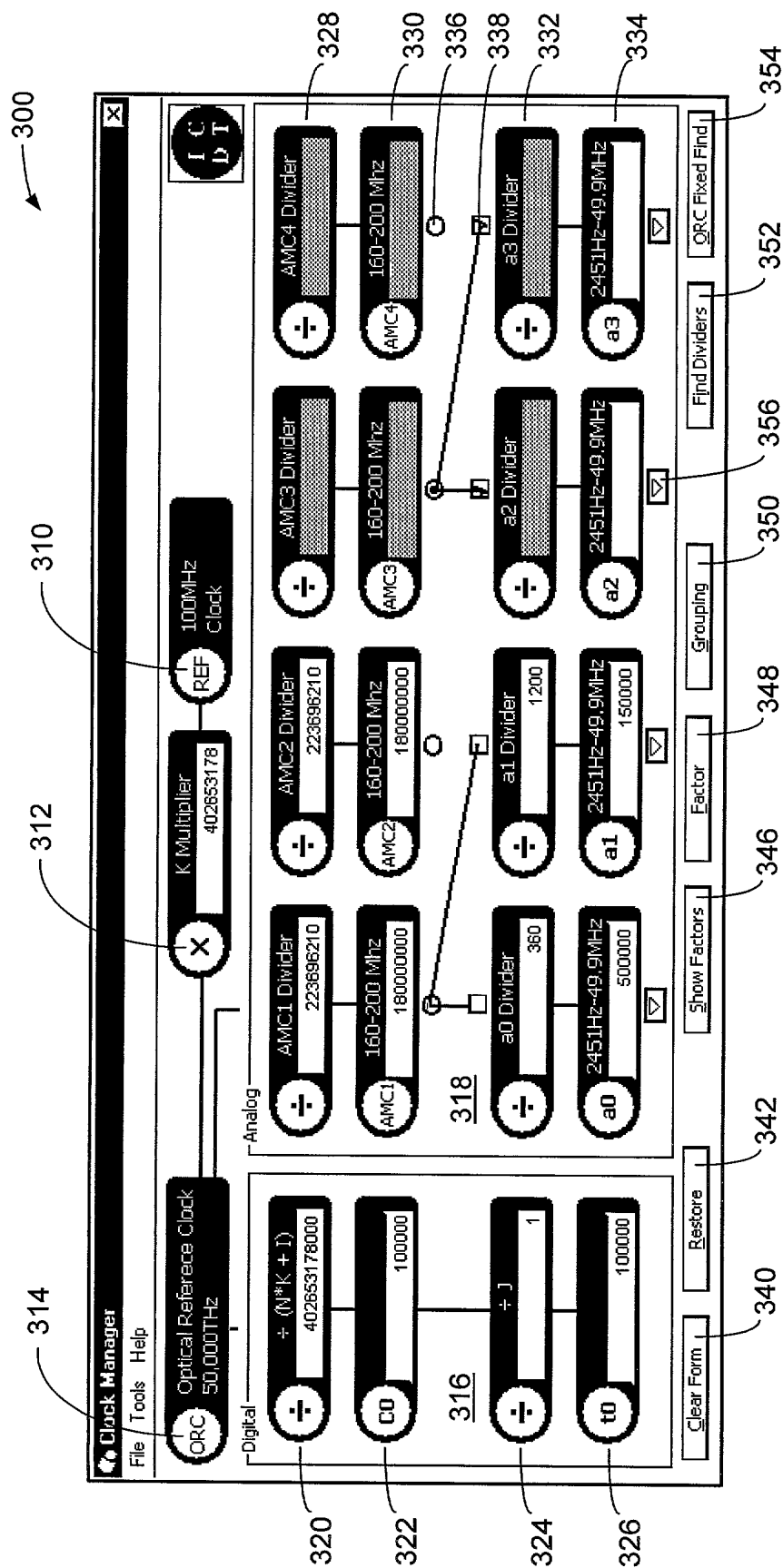


Fig. 4

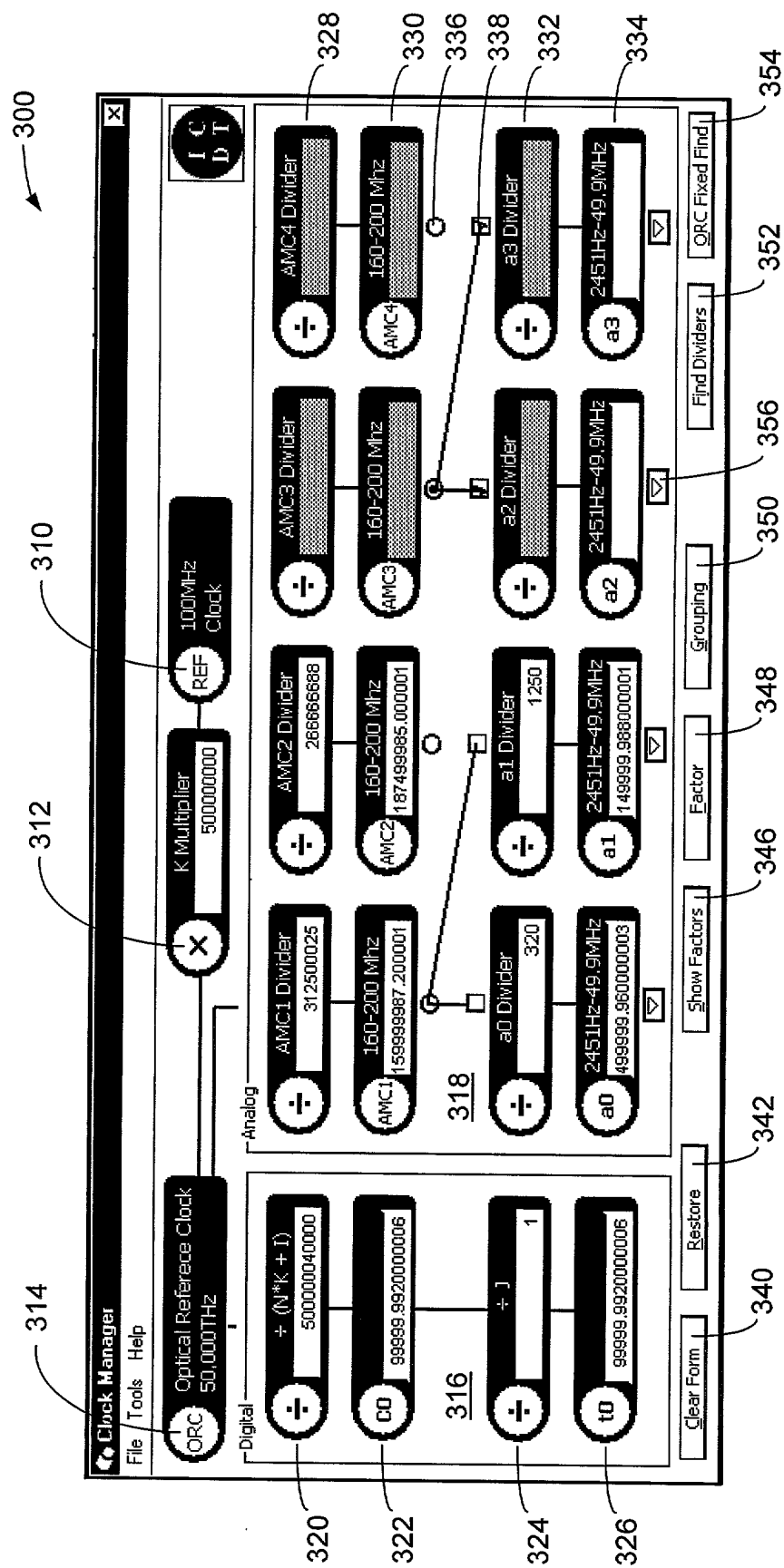


Fig. 5

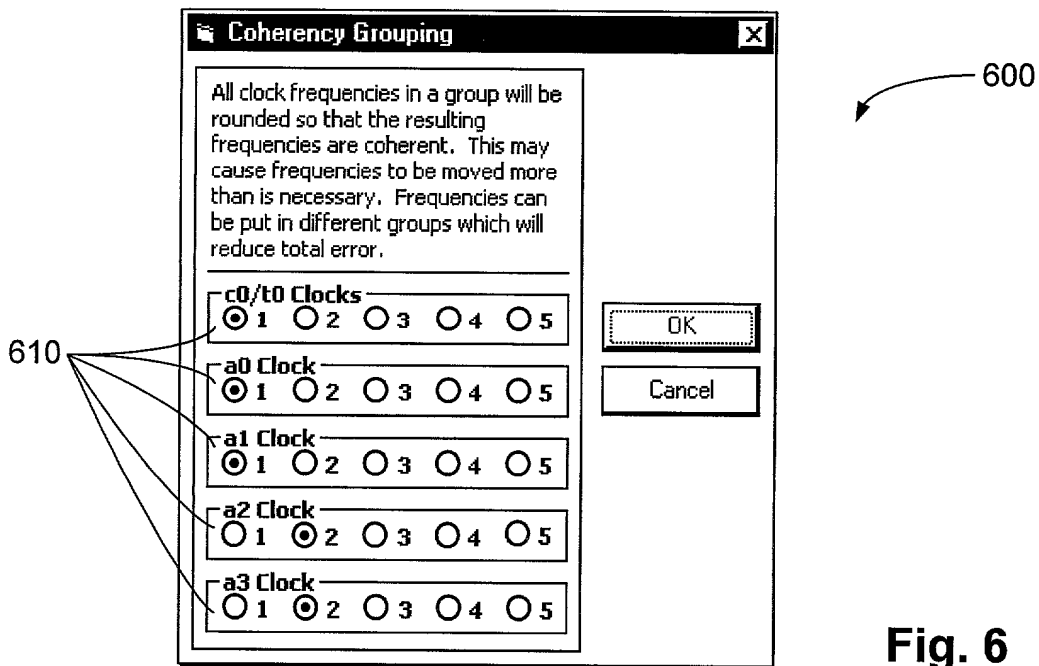


Fig. 6

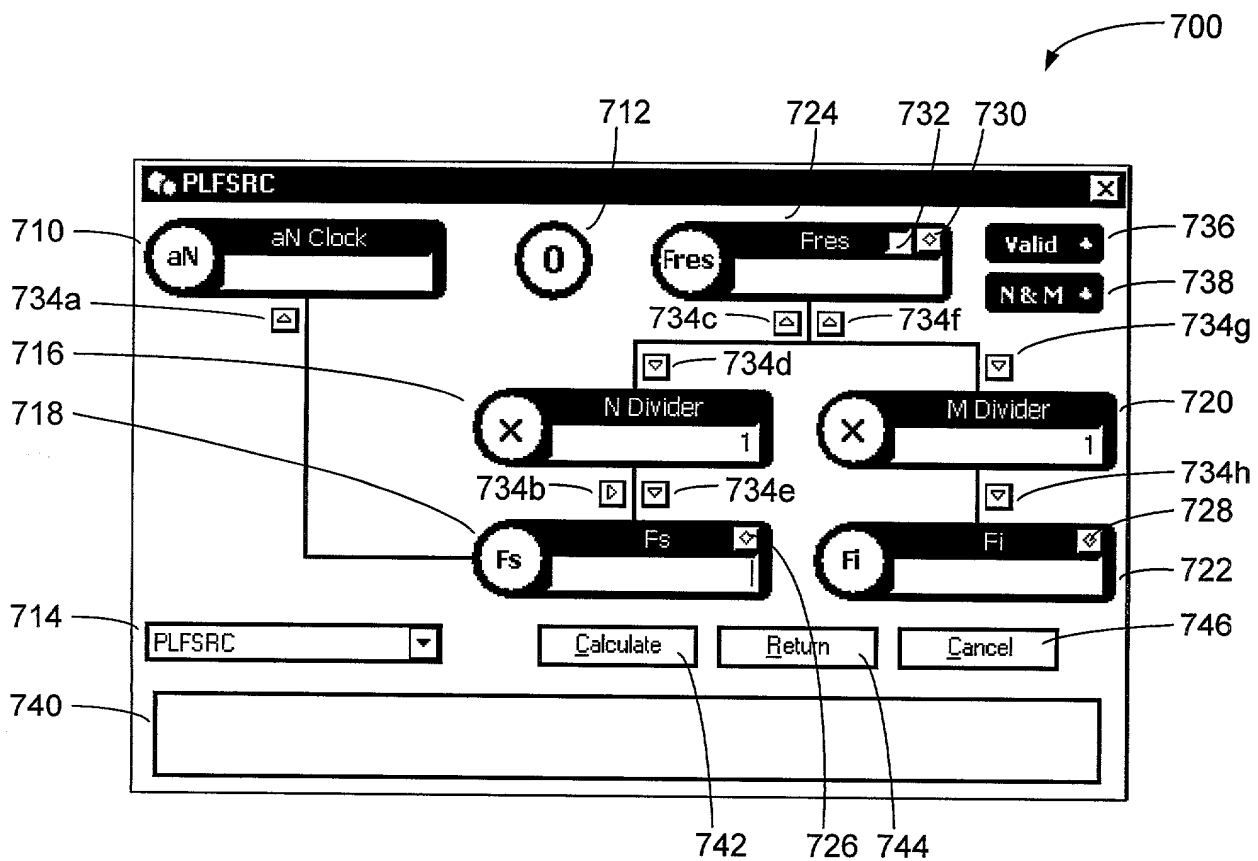


Fig. 7

PLFDIG

aN Clock: 100000000 0 Fres: 1000 Valid N & M

OSCD: 1 N Divider: 50000 M Divider: 3

Decimation Factor: 1 Fs: 50000000 Fi: 1000000

PLFDIG Calculate Return Cancel

Error: Fi Must Be Less Than 500k

Fig. 8

PLFDIG

aN Clock: 100000000 0 Fres: 1000 Valid N & M

OSCD: 1 N Divider: 50000 M Divider: 3

Decimation Factor: 1 Fs: 50000000 Fi: 3

PLFDIG Calculate Return Cancel

Fi Dependence
 Frequency equals = 3
 ÷ 2
 x
Analog Branch
☒ aN0 ☒ aN1
☒ aN2 ☒ aN3
Source
☒ Fs ☒ Fi
☒ Fres ☒ c0 ☒ t0
 Get Value

900

Fig. 9

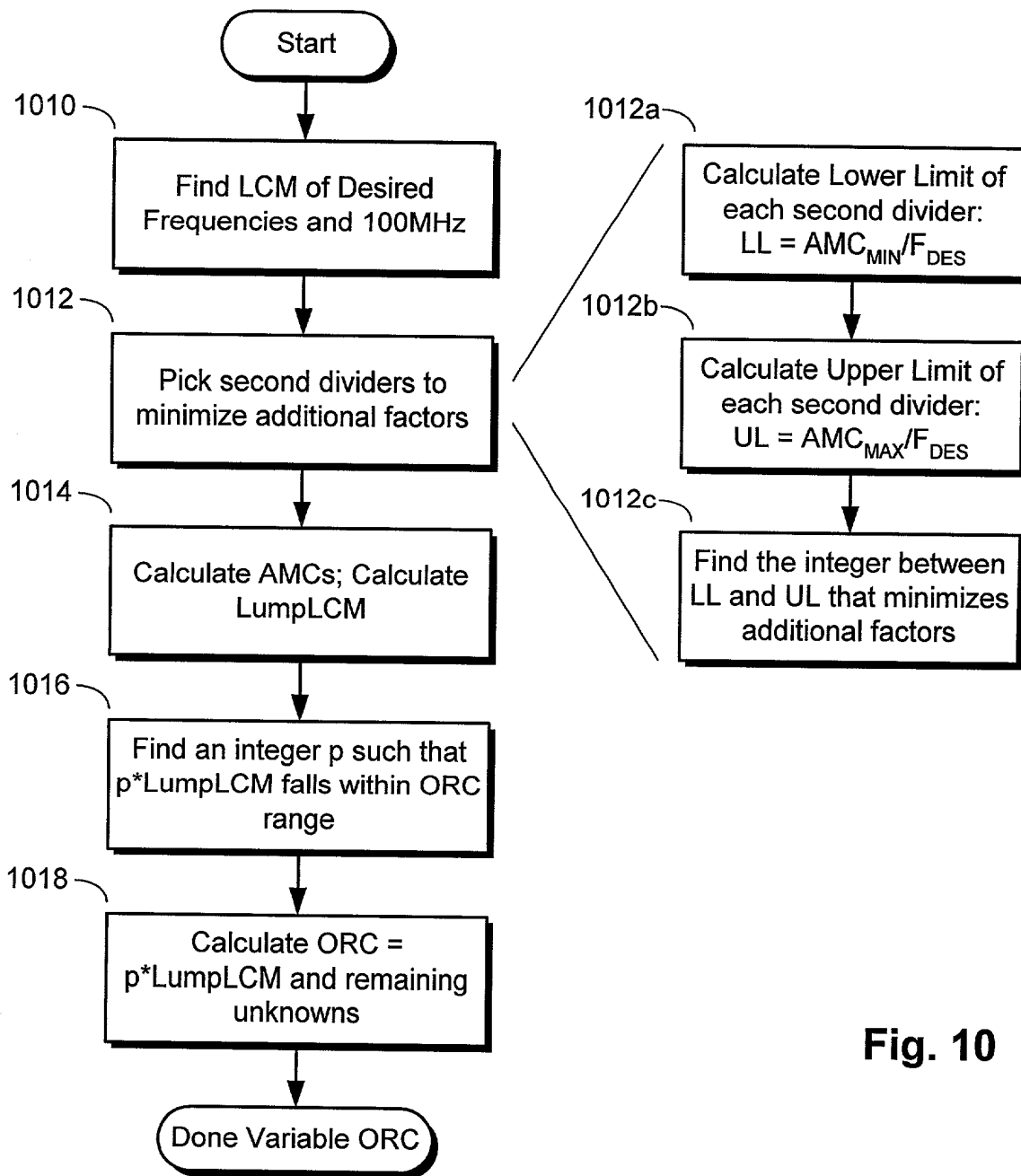


Fig. 10

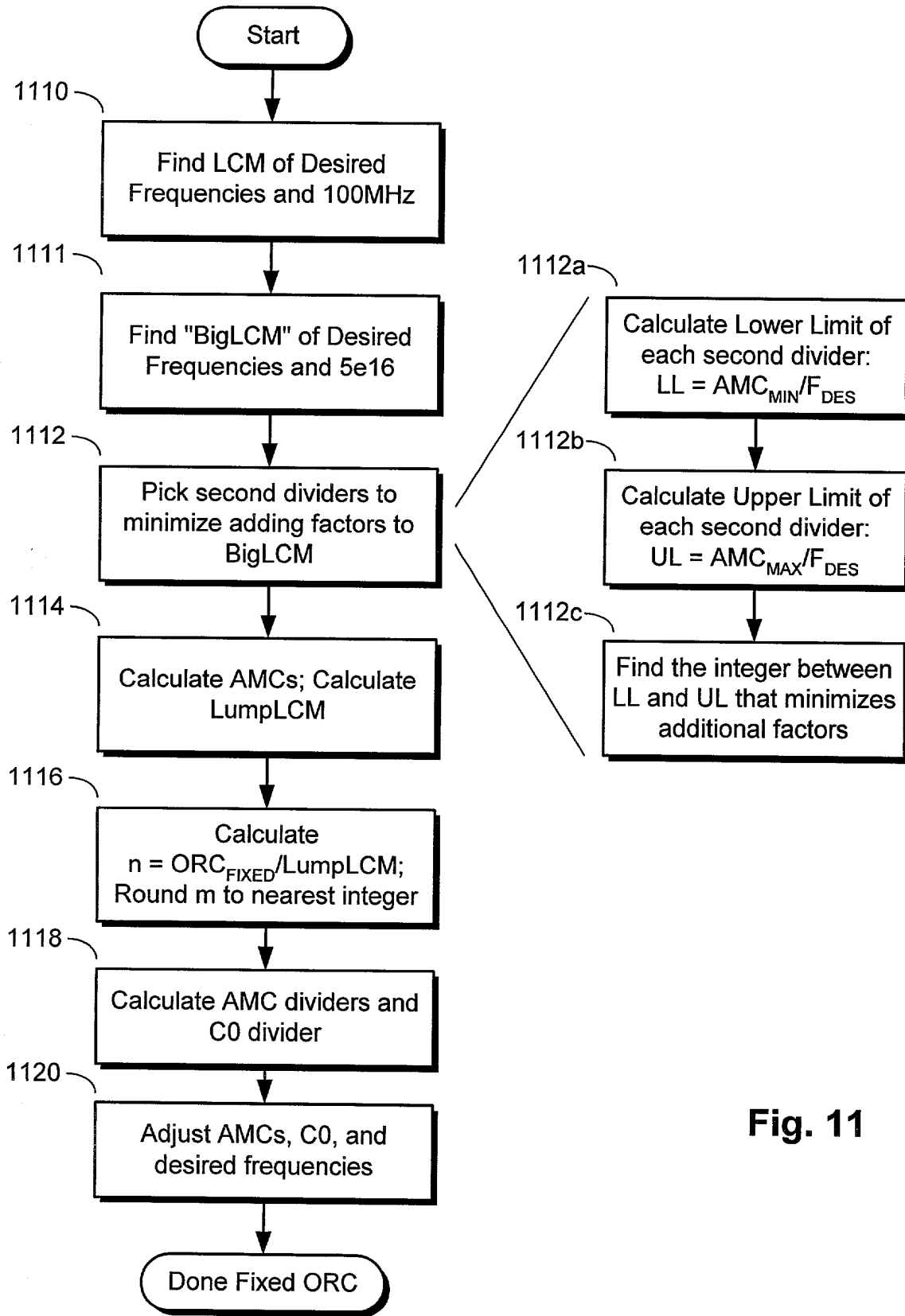


Fig. 11